



- REG1: $V_{out}=8.0V$.
- C1/R1: Sine wave signal $V_{max}=4.66V$, $V_{min}=1.6V$.
- C1/R1 set osc frequency
- C1/R1 $\sim 17.5kHz$
- VR2: PWM current adjust. on-off. 0-100% on gate circuit $\sim 5mA$ load use standard parts.
- R14+VR3 $adj=47k$
- VR2 sets min brightness
- VR3 sets max brightness at V_{in}

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 Drawn By: D. H.