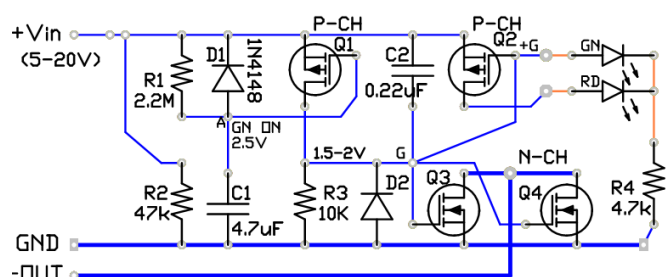
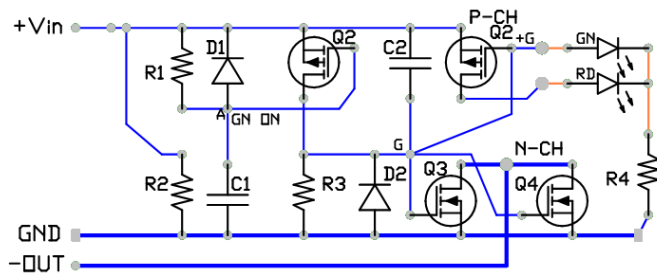
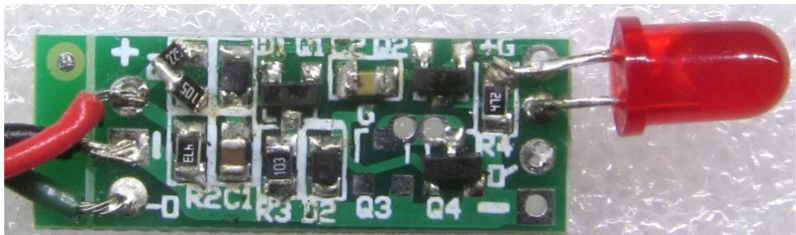
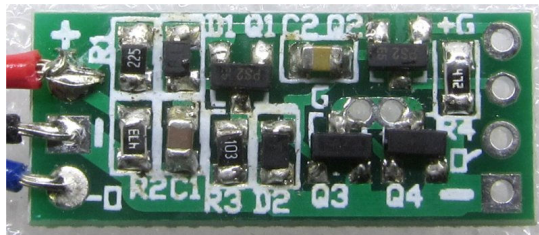
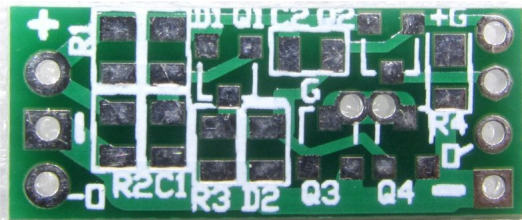
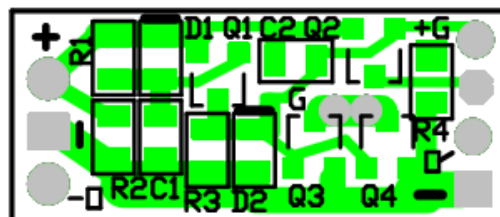
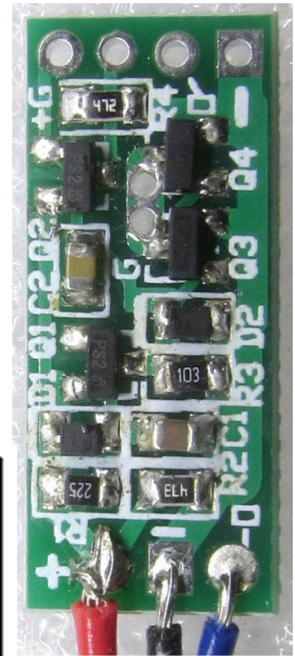


Onstate PEVDC PCB is a simple, reliable transistor-based voltage detector circuit that could be setup as a delay ON/OFF timer or voltage level indicator applications. The output could be LEDs or a relay for power switching.

PCB: 1/16" FR4, 1oz Cu, Lead-free, HASL SMOBC
 Plating: 2 layers PCB with through hole plating
 PCB Size: 0.40"x0.95"+/-0.1" (10mmx24mm)
 Pad Holes: 7x 35mil (0.89mm D)
 Panel: 5x10 V-scored
 Parts: 0805 and SOT-23 SMD

Part: PCB-PEVDC, PCB, voltage detector circuit, timer, ON/OFF CTRL



1. Schematic layout.

2. Time delay circuit (R1C1, 20s)

Notes: Change C1 to R for voltage level detector circuit. D1 could be C filter. Q2 (PNP) and Q3 (NPN) could be bipolar transistors for low voltage detection.