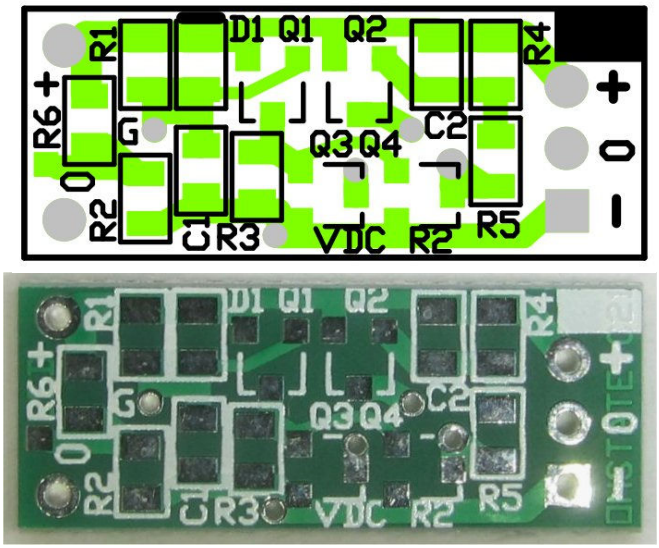
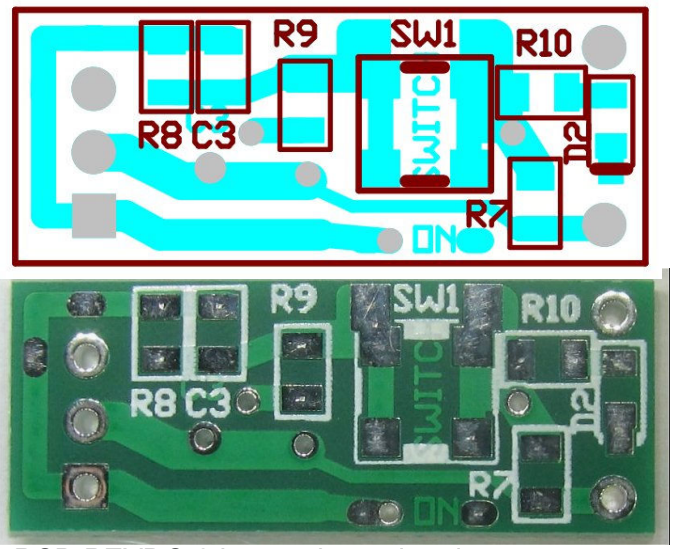
Onstate PEVDC PCB is a simple, reliable transistor-based voltage detector circuit that could be setup as a delay ON/OFF timer, latching switch, or voltage level indicator applications. The output could be LEDs, MOSFETs, or a relay for power switching.

PCB: 1/16" FR4, 1oz Cu, Lead-free, HASL SMOBC
Layout: 2 layers PCB with silkscreen both sides.
PCB Size: 0.40"x1.00"+/-0.01" (10.2mmx25.5mm)
Parts: 0805 and SOT-23 SMD

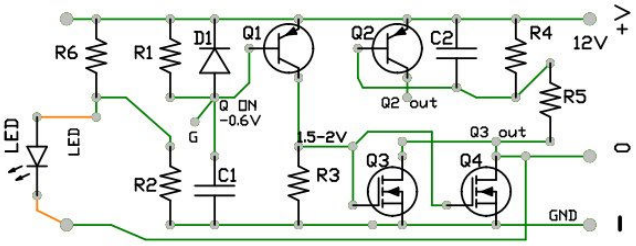


PCB-PEVDC-2 top legend and trace.

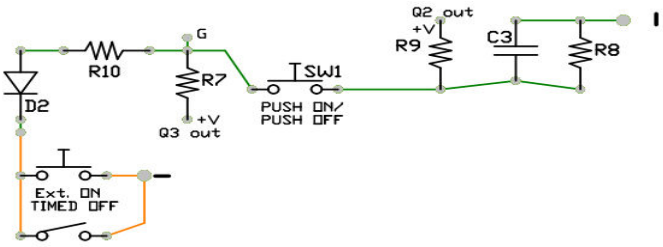


PCB-PEVDC-2 bottom legend and trace.

Schematic layout.



Top side schematic (LED is external wiring).



Bottom side schematic (switch to D2 is external).

Operation:
Q1= ON/OFF gate control. Q2/Q3= +V/- latch signal. R1/R4 holds Q1/Q2 high OFF. C2=time delay.